

OLIFF & BERRIDGE, PLC  
P.O. Box 19928  
Alexandria, Virginia 22320  
Telephone: (703) 836-6400  
Facsimile: (703) 836-2787



PATENT APPLICATION

Attorney Docket No.: 105030

AMENDMENT TRANSMITTAL

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/486,561

Group Art Unit: 2815

Examiner: L. Cruz

Filed: February 29, 2000

For: SEMICONDUCTOR DEVICE AND METHOD THEREOF, CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment in the above-identified application.

- ☐ Entitlement to small entity status is hereby asserted.  
☐ Small entity status of this application has been established.

The filing fee has been calculated as shown below:

	(Column 1)	(Column 2)	(Column 3)	SMALL ENTITY		OTHER THAN A SMALL ENTITY	
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADD'L FEE	RATE	ADD'L FEE
TOTAL CLAIMS	*15 MINUS	**20	=0	x 9	\$	x 18	\$
INDEP CLAIMS	*4 MINUS	***3	=1	x 42	\$	x 84	\$84
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE DEP. CLAIM				+ 140	\$	+ 280	\$
					\$		\$84

- \* If the entry in Column 1 is less than the entry in Column 2, write "0" in Column 3.  
\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.  
\*\*\* If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space.

The "Highest Number Previously Paid For" in this space (Total or Independent) is the highest number found from the equivalent box in Column 1 of a prior Amendment or the number of claims originally filed.

- ☒ Check No. 130611 in the amount of \$84 is attached. The Director is hereby authorized to charge any other fees that may be required to complete this filing, or to credit any overpayment, to Deposit Account No. 15-0461. Two duplicate copies of this sheet are attached.

DEPOSIT ACCOUNT USE  
AUTHORIZATION  
Please grant any extension  
necessary for entry;  
Charge any fee due to our  
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Respectfully submitted,

James A. Oliff  
Registration No. 27,075

Christopher W. Brown  
Registration No. 38,025

JAO:CWB/rxg

Date: May 13, 2002



#18  
Ant D  
J. White  
5-29-02  
**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

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AMENDMENT

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Washington, D.C. 20231

Sir:

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MAY 16 2002  
TECHNOLOGY CENTER 2800

In response to the Office Action mailed February 11, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claims 3, 9 and 17-20 without prejudice to or disclaimer of the subject matter contained therein.

Please replace claims 1, 2, 8, 14 and 15 as follows:

D1 1. (Twice Amended) A method of manufacturing a semiconductor device comprising:

a first step of interposing an adhesive between a surface of a substrate on which an interconnect pattern is formed and a surface of a semiconductor chip on which electrodes are formed; and

a second step in which pressure is applied between said semiconductor chip and said substrate, said interconnect pattern and said electrodes are electrically connected,